



# BD35035

## 21W SINGLE ADJUSTABLE DC/DC CONVERTER

22-26V<sub>IN</sub>, 5-35V<sub>OUT</sub>@0.6A

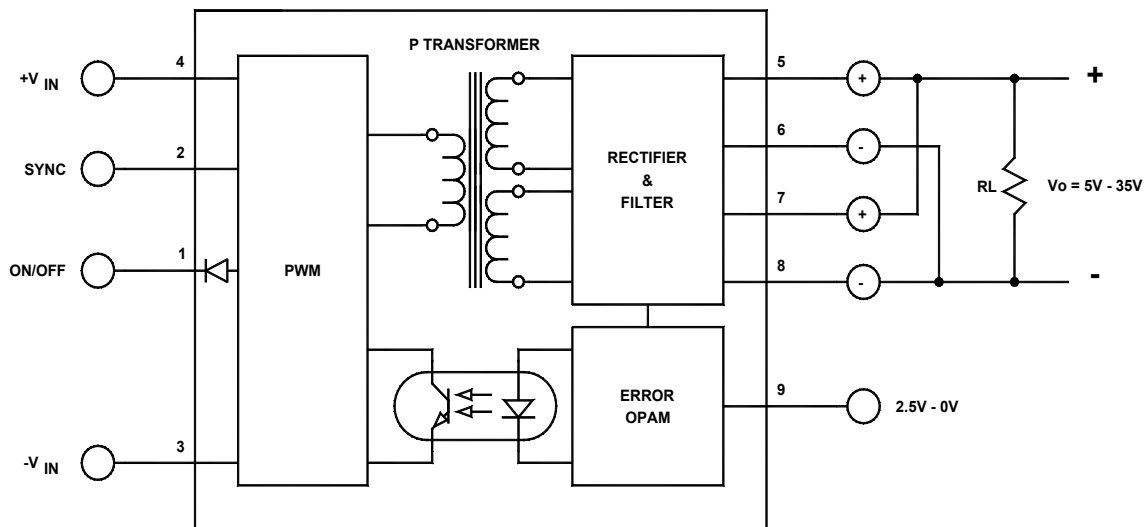
### Key Features

- 84% efficiency
- Input undervoltage protection
- Input-to-output isolation
- Soft start
- Dual short circuit protection
- 500µA off state current
- Multiple converter synchronization
- Adjustable outputs
- 330kHz switching frequency
- Thermal protection
- Six-sided shielding



### Functional Description

The BD35035 is an adjustable 21W converter. It accepts 22 to 26 V<sub>IN</sub> while its output can be set from 5 to 35 V<sub>OUT</sub> by an external voltage source ranging from 2.5 V to 0 V respectively.




Typical Block Diagram

## Electrical Specifications

### INPUT SPECIFICATIONS

Unless otherwise specified, all parameters are given under typical +25°C with nominal input voltage and under full output load conditions.

PARAMETER	CONDITION / NOTE	MIN	TYP	MAX	UNIT
Input Voltage Range		20	24	26	Vdc
Input Startup Voltage, 24V <sub>IN</sub>		17	18		Vdc
Input Overvoltage Protection, 24V <sub>IN</sub>		37	38		Vdc
Input Filter	Pi (π)				
Reverse Polarity 	External series-blocking diode				
Reflected Ripple	I <sub>o</sub> = FL, C <sub>IN</sub> = 10μF		100		mA
No Load Input Current			30		mA
Full Load Input Current			1562		mA
Input Surge Current (20μS Spike)				10	A
Short Circuit Current Limit	See Short Circuit Protection		150		% I <sub>IN</sub>
Off State Current			500		μA
Remote ON/OFF Control					
Supply ON	Pin 1 Open (Open circuit voltage: 13V max.)				
Supply OFF		0		0.8	Vdc
Logic Input Reference	-Input for ON/OFF and SYNC				
Logic Compatibility for Reference	TTL Open Collector or CMOS Open Drain				
Sync, High	See External Synchronization, Figure 3	2		6	Vdc
Sync, Low	See External Synchronization, Figure 3	0		0.8	Vdc

### OUTPUT SPECIFICATIONS

PARAMETER	CONDITION / NOTE	MIN	TYP	MAX	UNIT
Output Voltage		5		35	Vdc
Output Voltage Accuracy			±1	±2	%
Ripple & Noise			2	2	mV
Output Current		0	600	1200	mA
Line Regulation			±0.5	±1	%
Load Regulation			±1	±2	%
Temperature Coefficient @ FL			0.02		%/°C of V <sub>OUT</sub>
Transient Response Time	50% FL to FL to 50% FL to within 1% of V <sub>OUT</sub>		500		μS
Short Circuit Protection	By input current limiting				

### GENERAL SPECIFICATIONS

PARAMETER	CONDITION / NOTE	MIN	TYP	MAX	UNIT
Efficiency			84		%
Isolation Voltage (1 min.), Input to Output			1500		Vdc
Isolation Voltage (1 min.), Output to Output			500		Vdc
Isolation Resistance			10 <sup>9</sup>		Ω
Isolation Capacitance					pF
Switching Frequency			280	330	kHz
Turn On Delay	See Figure 5		7	10	mS
Soft Start Time	See Figure 5		7	15	mS

## ENVIRONMENTAL SPECIFICATIONS

PARAMETER	CONDITION / NOTE	MIN	TYP	MAX	UNIT
Operating Temperature Range (Ambient)*	See Figure 2	0		+71	°C
Storage Temperature Range		-55		+125	°C
Thermal Resistance	°C per watt internally dissipated		7	0	°C/W <sub>DISS</sub>
Maximum Operating Case Temperature				110	°C
Derating	See Figure 2				
Cooling	Free-air convection				
EMI/RFI	Six-sided continuous shielded metal case				
MTBF	per MIL-HNBK-217F (Ground benign, +25°C)		1.1×10 <sup>6</sup>		hours
Humidity	Up to 95% non-condensing				
Thermal Shutdown	Case Temperature		110	115	°C
Thermal Hysteresis		2	5		°C

\* See footnotes 3, 4, 5 and 6

## PHYSICAL CHARACTERISTICS

PARAMETER	CONDITION / NOTE	MIN	TYP	MAX	UNIT
Dimensions (L×W×H)	2.00×2.00×0.50 in. (50.80×50.80×12.70mm)				
Weight	2.43 oz. (69g)				
Case Material	Coated metal				
Shielding Connection, 24V <sub>IN</sub>	-V <sub>IN</sub> (Pin 3)				

<sup>1</sup> ⚠ WARNING - Usage of input fuse with adequate ratings is essential to avoid possible hazard and damage of the unit. A suppressor diode with adequate ratings is intended to be connected in series to the supply for reverse polarity protection.

<sup>2</sup> Measured with 10μF capacitor at the input power pins.

<sup>3</sup> The maximum input current at any given input range measured at minimum input voltage is given as 1.6\*I<sub>NOMINAL</sub>. Nominal input current is the typical value measured at the input of the converter under full-load room temperature and nominal input voltage (24Vdc).

<sup>4</sup> Contact factory for -55° to +85°C operating temperature range.

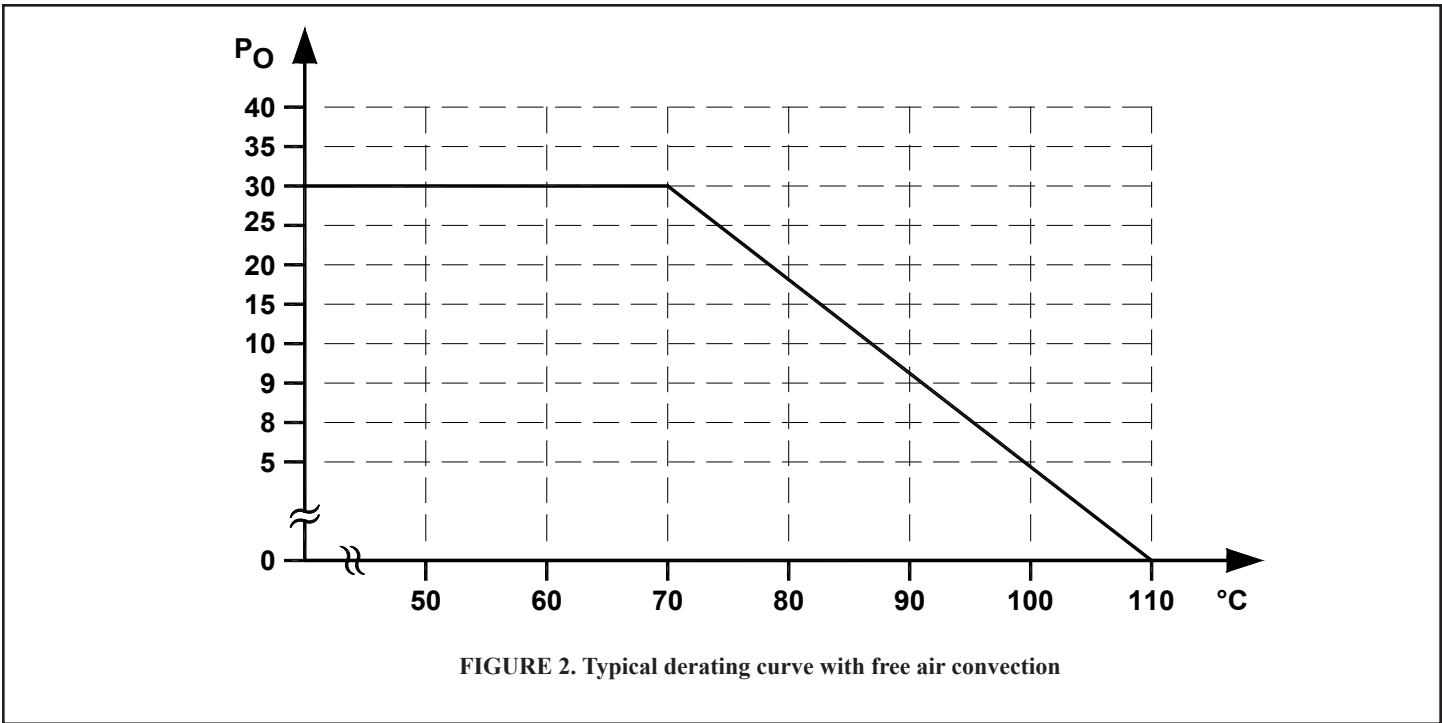
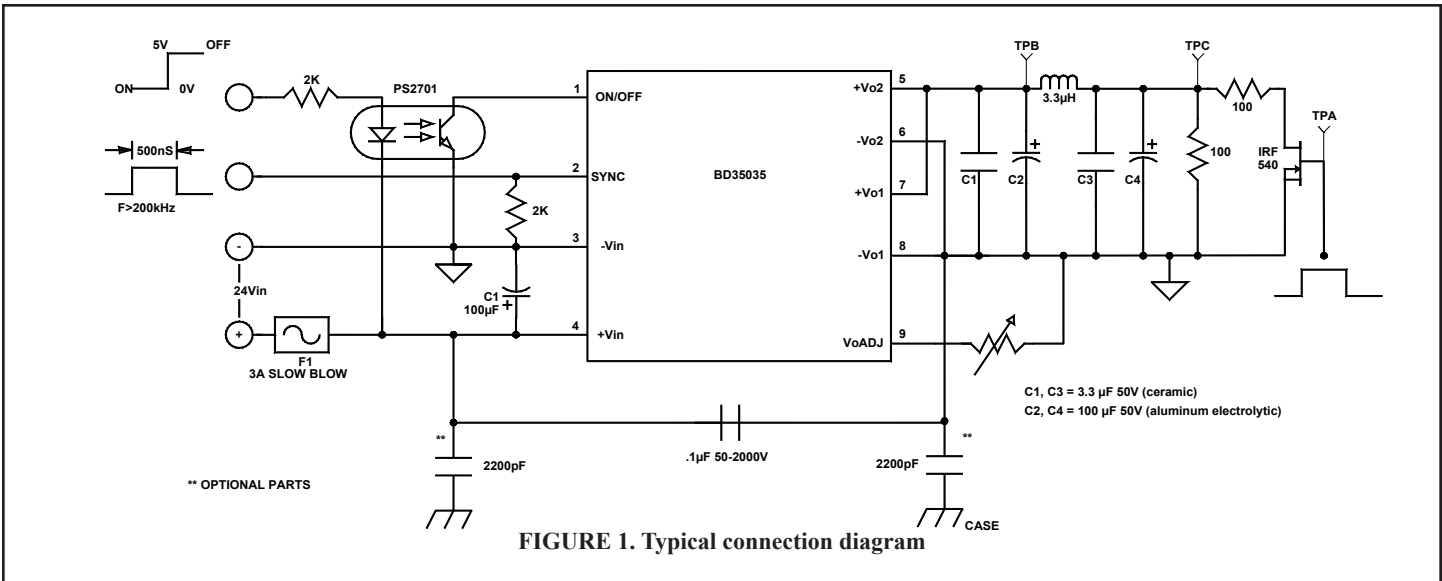
<sup>5</sup> Adequate insulation is to be provided to the converters at the end usage as per applicable requirements.

<sup>6</sup> Temperature rise on the case of the converters is to be considered during the end usage as per applicable requirements.

<sup>7</sup> For 24V<sub>IN</sub>, derate the converter above 70°C ambient (see Figure 2).

<sup>8</sup> The converter will operate over 2:1 (18-36) input voltage range with reduced output power.

<sup>9</sup> The input current limit of this converter will limit the output power to 40W to achieve 10V/ms rise time with the output filter shown in Figure 1.



### EXTERNAL SYNCHRONIZATION

A TTL signal applied at the SYNC pin of the converter will synchronize the switching frequency of the converter to that of the TTL input signal. The external (TTL) frequency must be equal or higher than the converter's frequency. At the positive-going edge of the applied pulse, the internal power-switching transistor turns off and the PWM discharges its timing capacitor. At the negative-going edge, the PWM resumes normal operation. The minimum positive pulse width of the TTL signal must be 300nS minimum and its frequency

between 150kHz and 180kHz. NOTE: Higher frequencies will reduce the efficiency of the converter and wide TTL pulses will force the PWM to follow the external TTL width modulation, which may effect regulation. A high TTL signal at the SYNC pin of the converter will turn the converter off. An internal pull-down resistor will keep this pin low when it is not used. A pulse differentiator (see Figure 4) can be used to shape a square wave sync signal as shown in Figure 3.

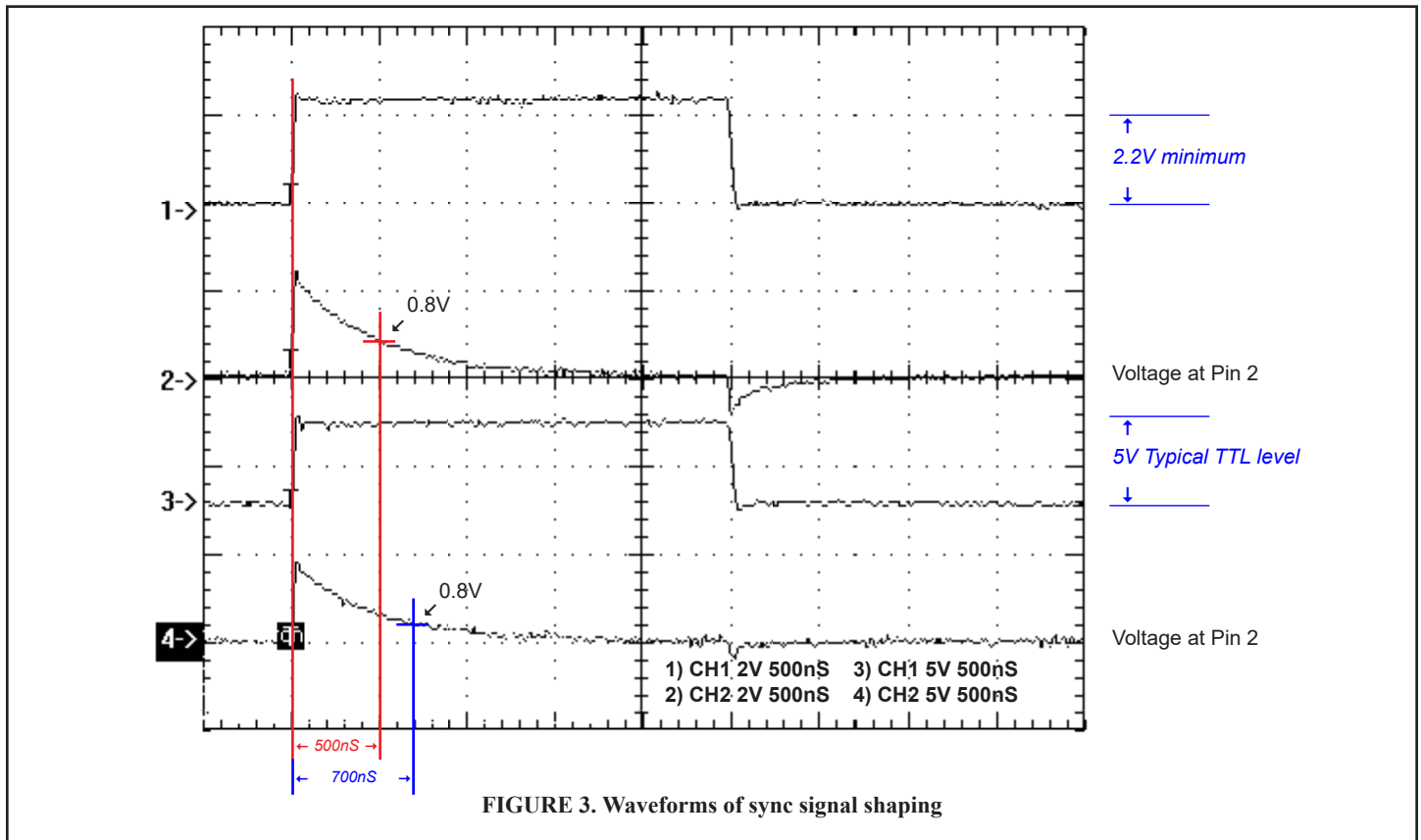


FIGURE 3. Waveforms of sync signal shaping

### SYNC SIGNAL SHAPING

As described in External Synchronization, the PWM of the converter requires a TTL signal of 0.8 to 2Vdc minimum amplitude and minimum duration of 300nS. When such a signal is not available (through one shot multivibrator or other pulse-shaping circuits) a C-R differentiator, such as the one in Figure 3, can be used to shape a square wave TTL signal. As is shown by the oscillogram in Figure

3, the positive edge of the sync pulse must be 2V minimum and the decaying exponential must reach the low 0.8Vdc in 300nS minimum from the positive edge. The parallel diode with the resistor is a small signal switching diode or a Schottky signal diode with 0.3 to 0.5V forward drop, it is used to clamp the voltage at pin 2@-0.5Vdc.

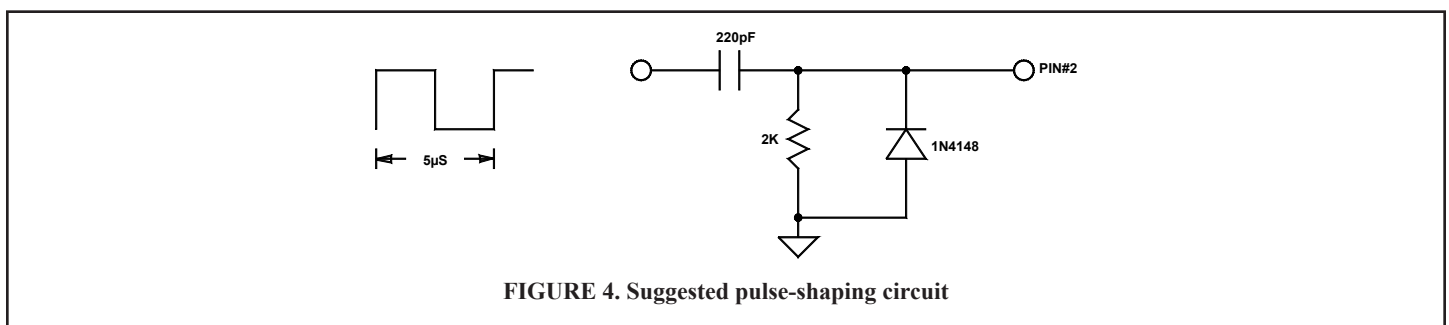


FIGURE 4. Suggested pulse-shaping circuit

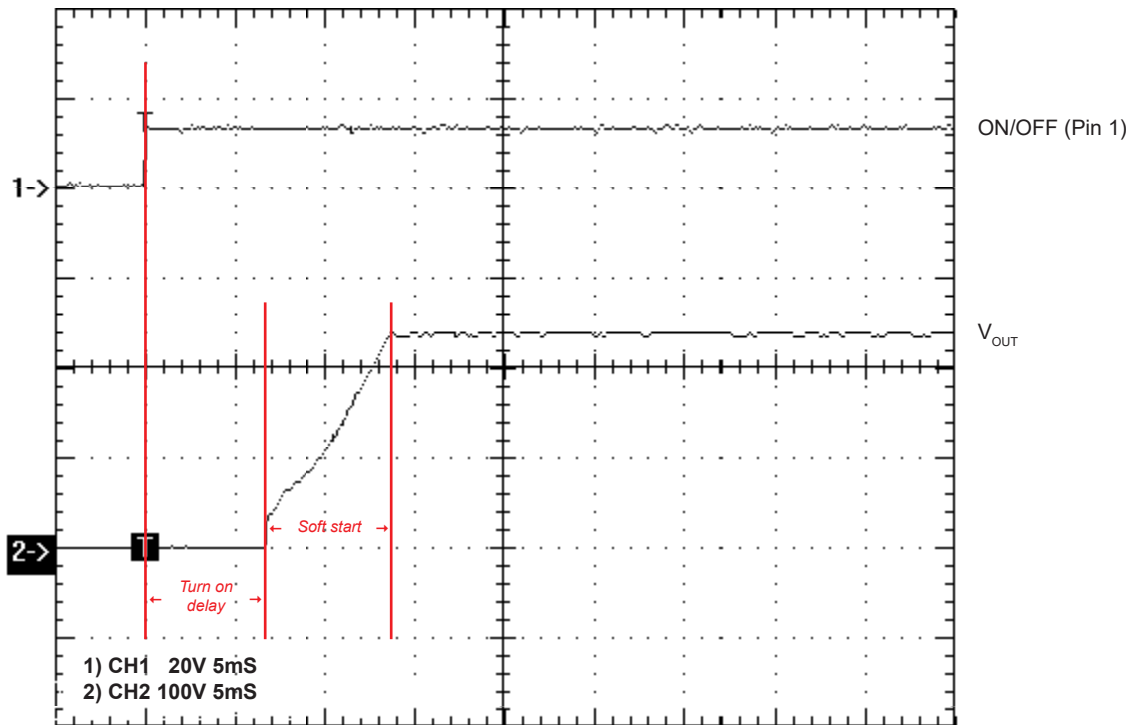


FIGURE 5. Turn on delay with soft start

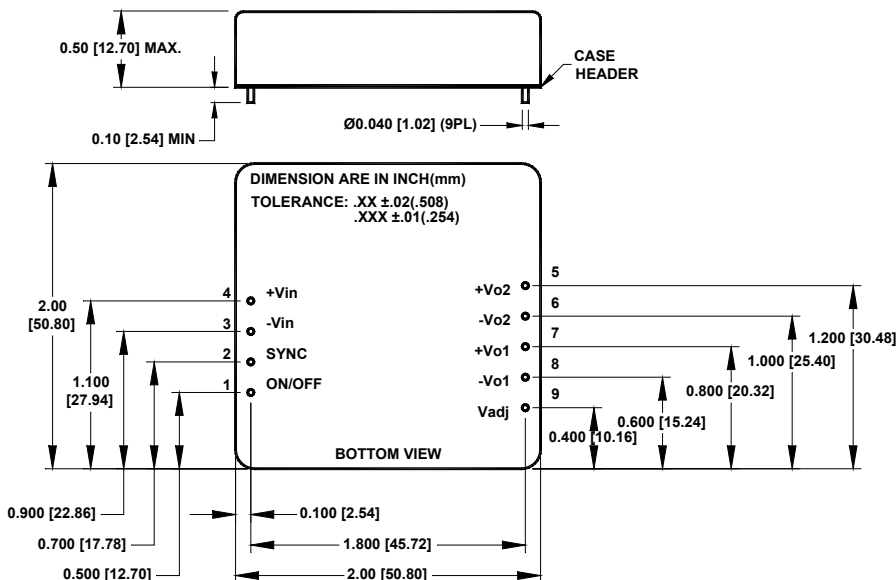
### SHORT CIRCUIT PROTECTION

The converter has a dual short circuit protection feature. At the input side of the converter, two short circuit current comparators are used to monitor the input current of the converter. They are biased at different voltage levels; the lower threshold (LTH) comparator provides the power limiting function of the converter. Under normal operating conditions, the LTH comparator limits the output power of the converter when the maximum output power is exceeded.

When a hard short is applied across the output of the converter and the input current exceeds the set threshold of the second

comparator, the converter goes into shutdown mode, the overcurrent latch is set and the converter is turned off. The converter will turn on again when its input voltage is recycled (OFF-ON) or if the ON/OFF pin is used to turn the converter on and off. The time required for the ON/OFF pin to be held low is between 100mS and 800mS.

### MECHANICAL SPECIFICATIONS



Pin	Function
	<b>SINGLE</b>
1	ON/OFF
2	SYNC
3	-V <sub>IN</sub>
4	+V <sub>IN</sub>
5	+V <sub>O2</sub> (CONNECT TO PIN 7)
6	-V <sub>O2</sub> (CONNECT TO PIN 8)
7	+V <sub>O1</sub> (CONNECT TO PIN 5)
8	-V <sub>O1</sub> (CONNECT TO PIN 6)
9	V <sub>OUT</sub> ADJ

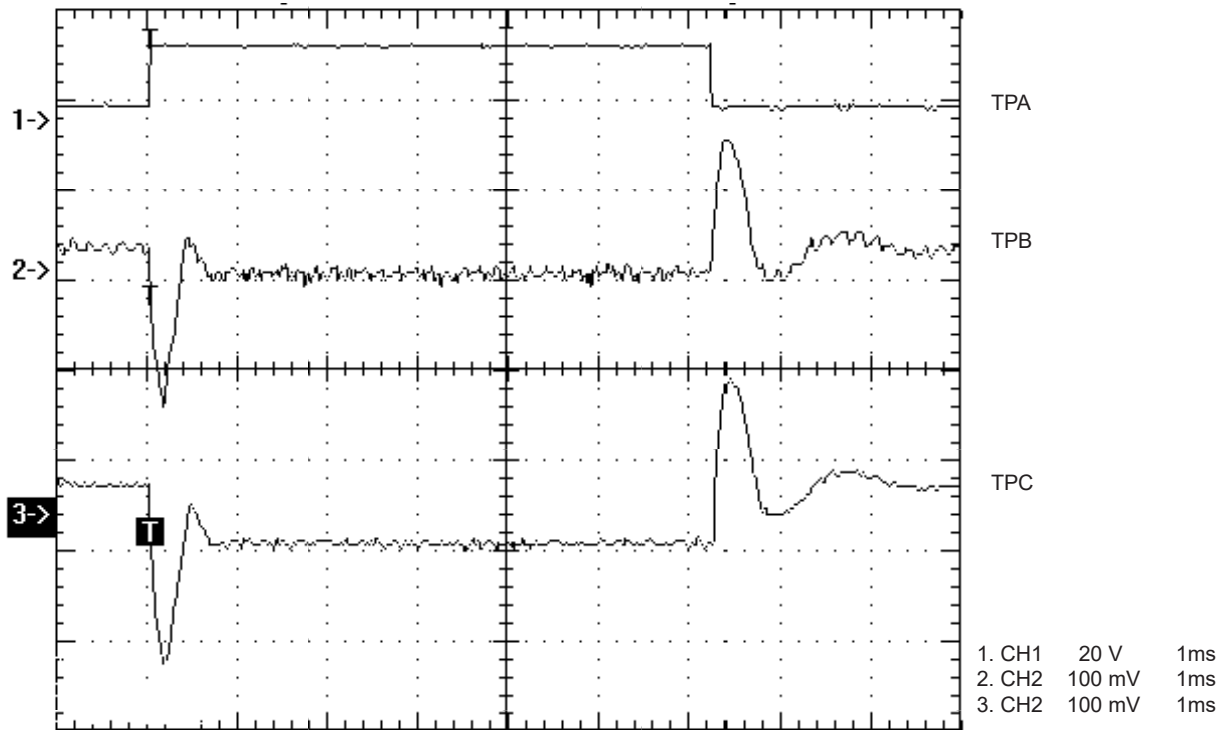


FIGURE 6. Load Step Response from 40% FL to 80% FL to 40% FL

$V_o = 35V$

See Figure 1 for Test Points A, B, and C

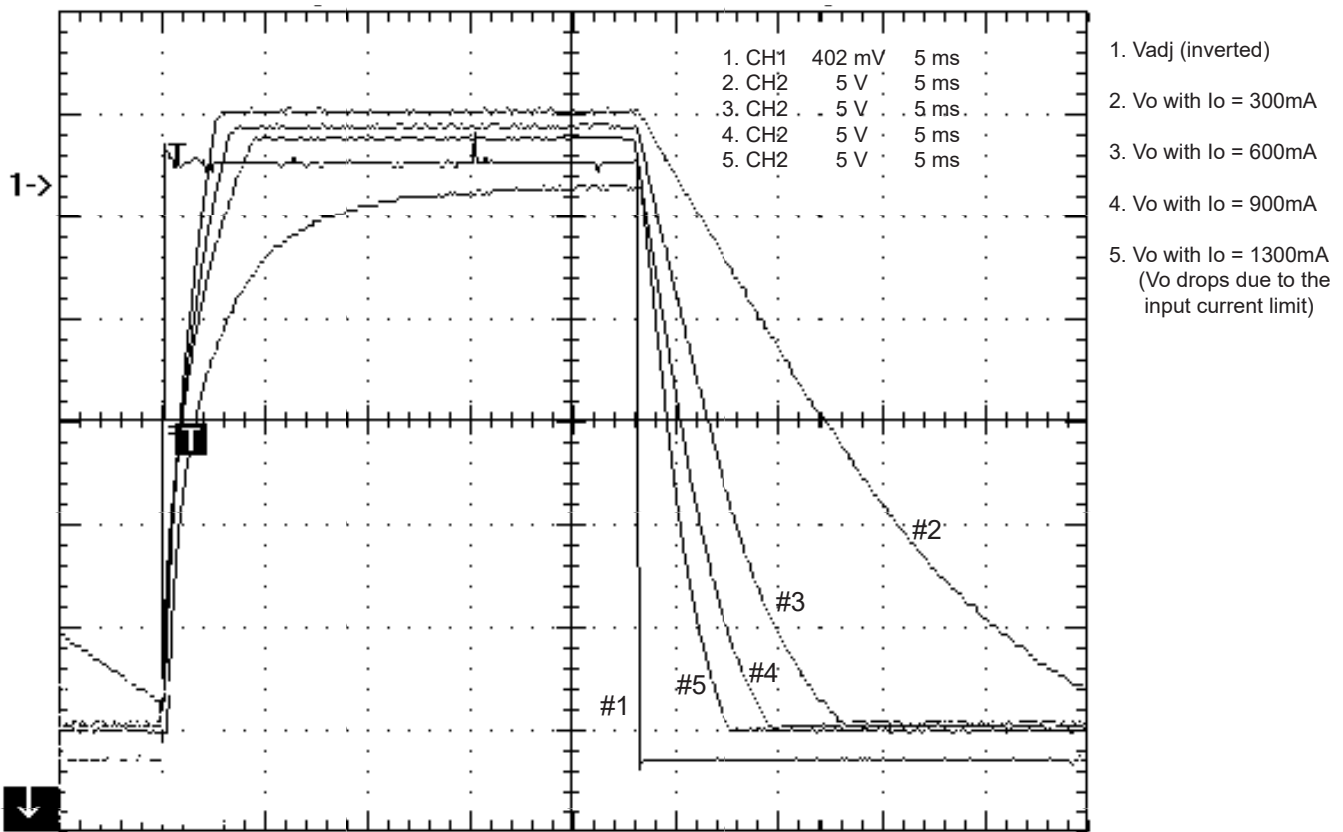
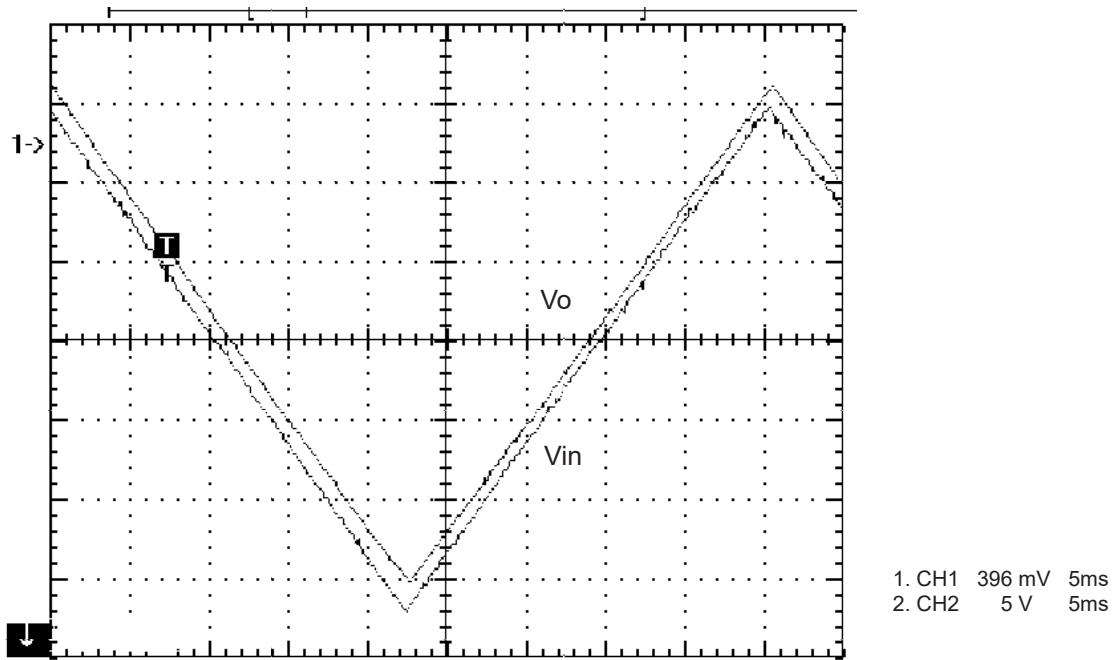
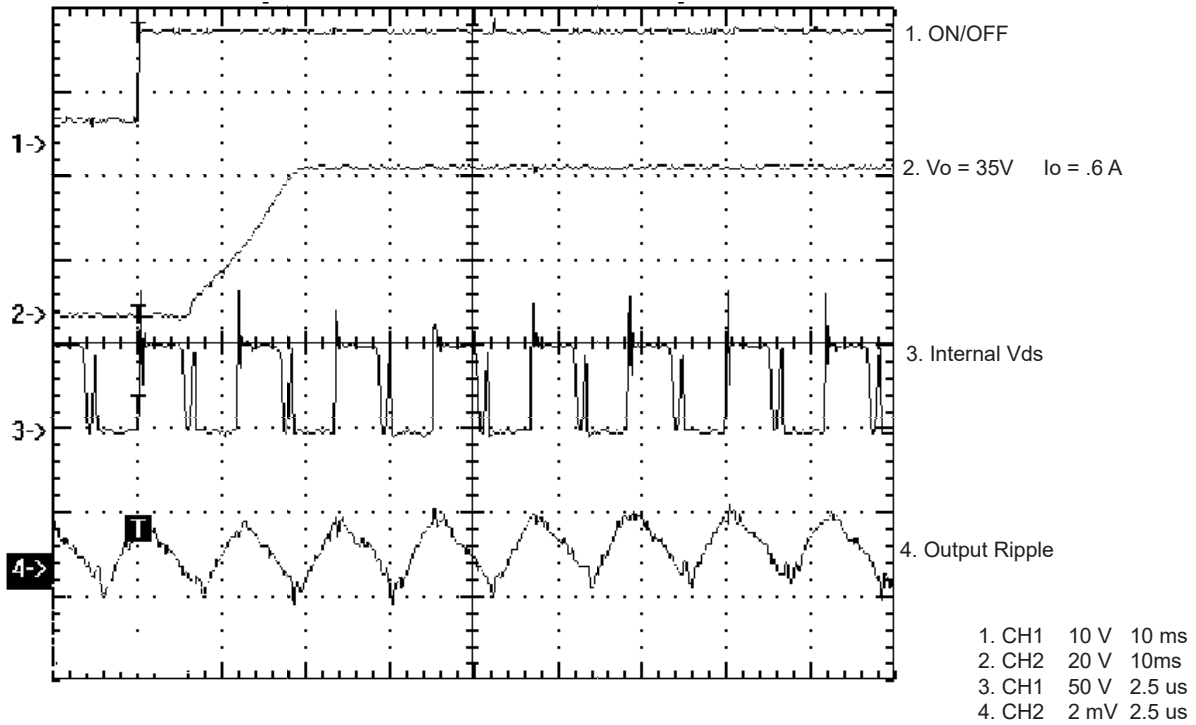


FIGURE 7.  $V_{adj}$  and  $V_{out}$  with various current levels



**FIGURE 8. Tracking of  $V_{in}$  (inverted) and  $V_{out}$  with  $I_o = 900\text{mA}$  (set through electronic load)**



**FIGURE 9. Turn on delay with soft start and output ripple with the output filter given in Figure 1**

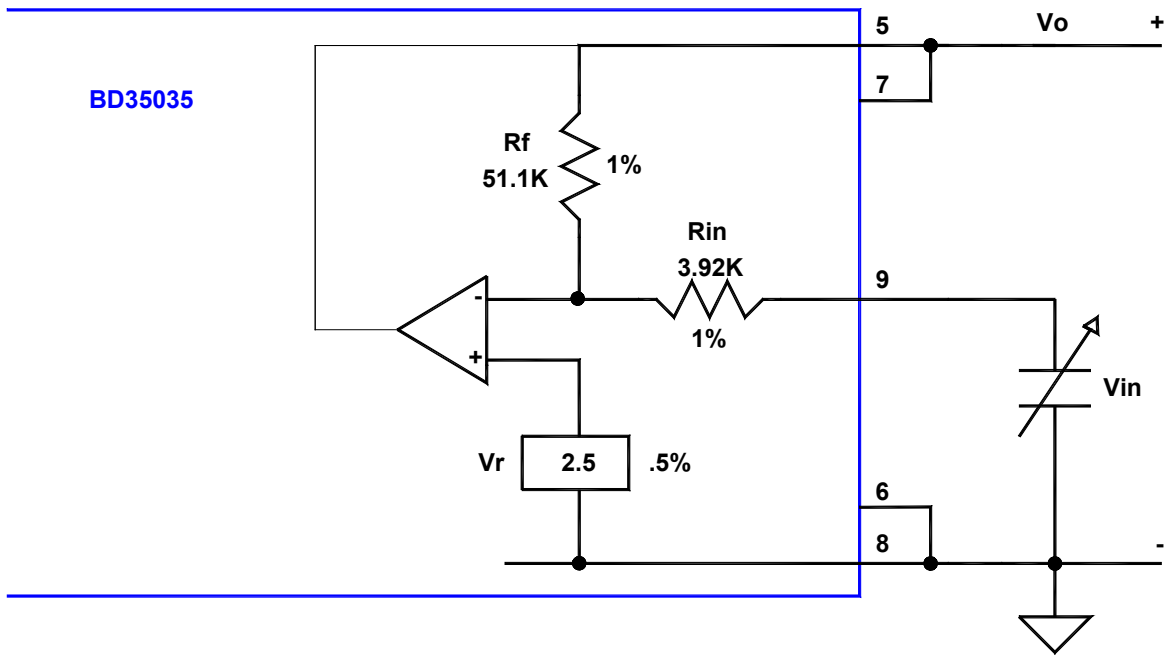


FIGURE 10. Under Voltage Output Trim

$$V_O = V_R (1 + R_F / R_{IN}) - (R_F / R_{IN}) V_{IN} = 35.09 - 13.036 V_{IN}$$

$$0 \text{ V} \geq V_{IN} \geq 2.5 \text{ V}$$