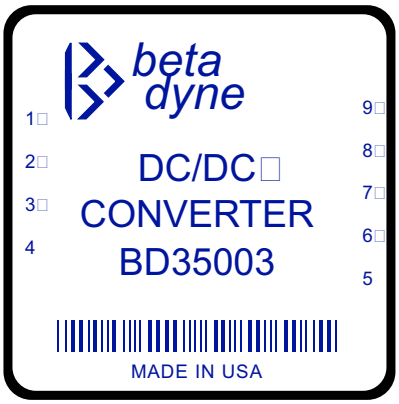
BD35003

35W HIGH-VOLTAGE DC/DC CONVERTER

Dual ±120V_{OUT}

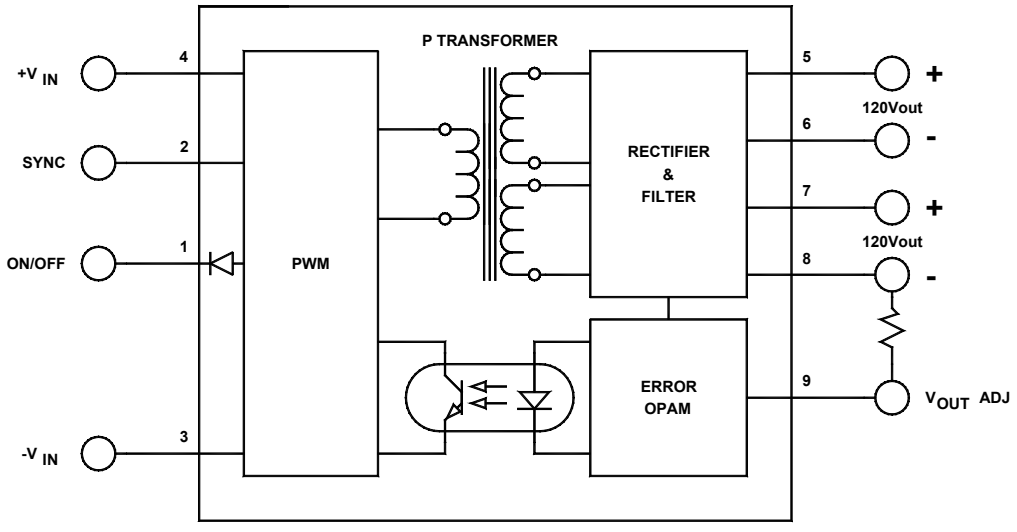
Key Features

- 86% efficiency
- Input under/overvoltage protection
- Input-to-output isolation
- Soft start
- Dual short circuit protection
- 500µA off-state current
- Multiple converter synchronization
- Adjustable outputs
- 170kHz switching frequency
- Thermal protection
- Six-sided shielding



Functional Description

The BD35003 is a 35W High-Voltage DC/DC Converter with a nominal input voltage of 24V_{IN} (range of 18–36V_{IN}) and an output of ±120V_{OUT}@±146mA. The converter is designed to operate with unbalanced loads of 1mA/84mA with a maximum output voltage accuracy of ±1.5%.



Typical Block Diagram

Electrical Specifications

Unless otherwise specified, all parameters are given under typical +25°C with nominal input voltage and under full output load conditions.

INPUT SPECIFICATIONS

PARAMETER	CONDITION / NOTE	MIN	TYP	MAX	UNIT
Input Voltage Range		18	24	36	Vdc
Input Startup Voltage				17	Vdc
Input Overvoltage Protection		37			Vdc
Input Filter	Pi (π)				
Reverse Polarity	External series-blocking diode				
Reflected Ripple	I_O = FL, C_{IN} = 10µF		100		mA_{PP}
No Load Input Current			40		mA
Full Load			1700		
Input Surge Current (20µS Spike)				10	A
Short Circuit Current Limit	See Short Circuit Protection		150		% I_{IN}
Off State Current			500		µA
Remote ON/OFF Control					
Supply ON	Pin 1 Open (Open circuit voltage: 13V max.)				
Supply OFF		0		0.8	Vdc
Logic Input Reference	-Input for ON/OFF and SYNC				
Logic Compatibility for Reference	TTL Open Collector or CMOS Open Drain				
Sync, High	See External Synchronization, Figure 3	2		6	Vdc
Sync, Low	See External Synchronization, Figure 3	0		0.8	Vdc

OUTPUT SPECIFICATIONS

PARAMETER	CONDITION / NOTE	MIN	TYP	MAX	UNIT
Output Voltage			±120		Vdc
Output Voltage Accuracy			±1	±1.5	%
Output Current			±146		mA
Ripple & Noise			1	2	$\%V_{PP}$ of V_{OUT}
Line Regulation, Dual			±0.5	±1	%
Load Regulation, Dual			±1	±2	%
Temperature Coefficient @ FL			0.02		%/°C
Transient Response Time	50% FL to FL to 50% FL		250		µS
Short Circuit Protection	By input current limiting				
Output Adjust Range		±5		±10	%

GENERAL SPECIFICATIONS

PARAMETER	CONDITION / NOTE	MIN	TYP	MAX	UNIT
Efficiency			86		%
Isolation Voltage (1 min.), Input to Output			1500		Vdc
Isolation Resistance			10^9		Ω
Isolation Capacitance			2700		pF
Switching Frequency			170		kHz
Turn On Delay	See Figure 5		7	10	mS
Soft Start Time	See Figure 5		7	15	mS

PHYSICAL CHARACTERISTICS

PARAMETER	CONDITION / NOTE	MIN	TYP	MAX	UNIT
Dimensions (L×W×H)	2.00×2.00×0.50 in. (50.80×50.80×12.70mm)				
Weight	2.43 oz. (69g)				
Case Material	Coated metal				
Shielding Connection, $24V_{IN}$	$-V_{IN}$ (Pin 3)				

ENVIRONMENTAL SPECIFICATIONS

PARAMETER	CONDITION / NOTE	MIN	TYP	MAX	UNIT
Operating Temperature Range (Ambient)*	See Figure 2	-40		+71	°C
Storage Temperature Range		-55		+125	°C
Thermal Resistance	°C per watt internally dissipated		7		$°C/W_{DISS}$
Maximum Operating Case Temperature				110	°C
Derating	See Figure 2				
Cooling	Free-air convection				
EMI/RFI	Six-sided continuous shielded metal case				
MTBF	per MIL-HNBK-217F (Ground benign, +25°C)		$1.1×10^6$		hours
Humidity	Up to 95% non-condensing				
Thermal Shutdown	Case Temperature		110	115	°C
Thermal Hysterisis		2	5		°C

* See footnotes 3 and 4

[1] Measured with 10µF capacitor at the input power pins.

[2] The maximum input current at any given input range measured at minimum input voltage is given as $1.6*I_{NOMINAL}$. Nominal input current is the typical value measured at the input of the converter under full-load room temperature and nominal input voltage (24Vdc).

[3] Adequate insulation is to be provided to the converters at the end usage as per applicable requirements.

[4] Temperature rise on the case of the converters is to be considered during the end usage as per applicable requirements.

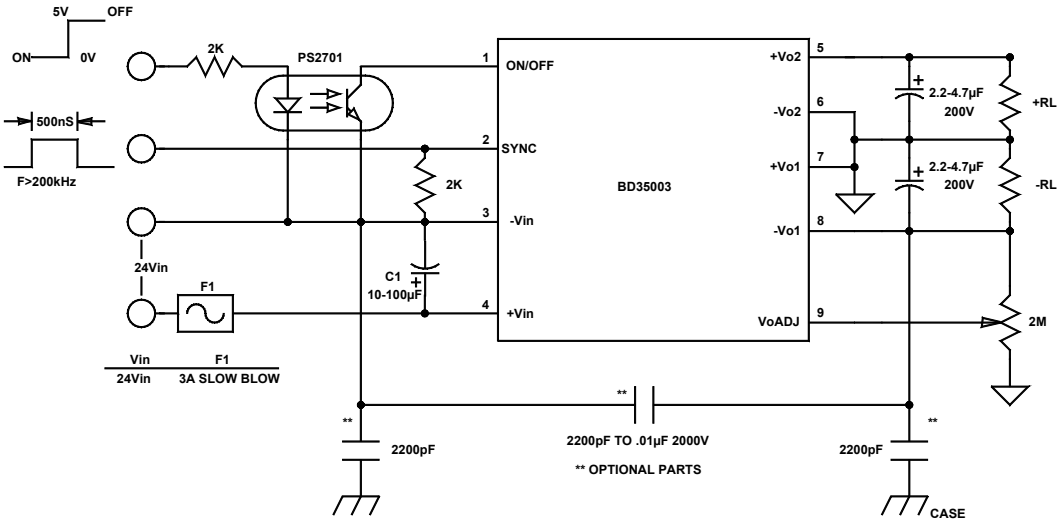


FIGURE 1. Typical connection diagram

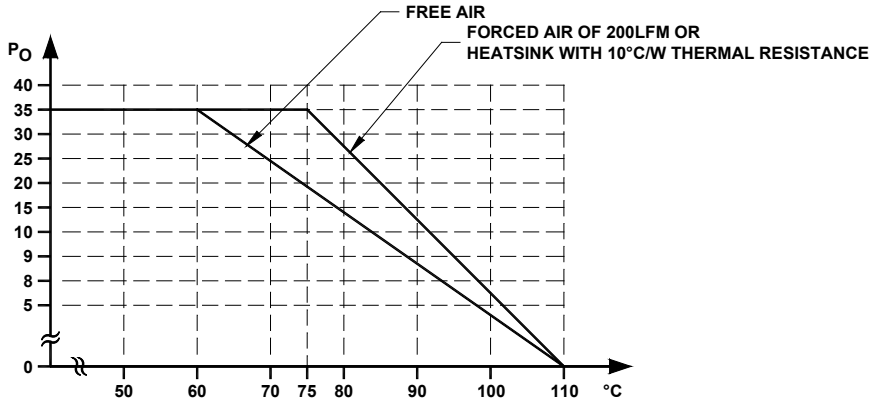


FIGURE 2. Typical derating curves

EXTERNAL TRIMMING OF OUTPUT VOLTAGES (DUAL)

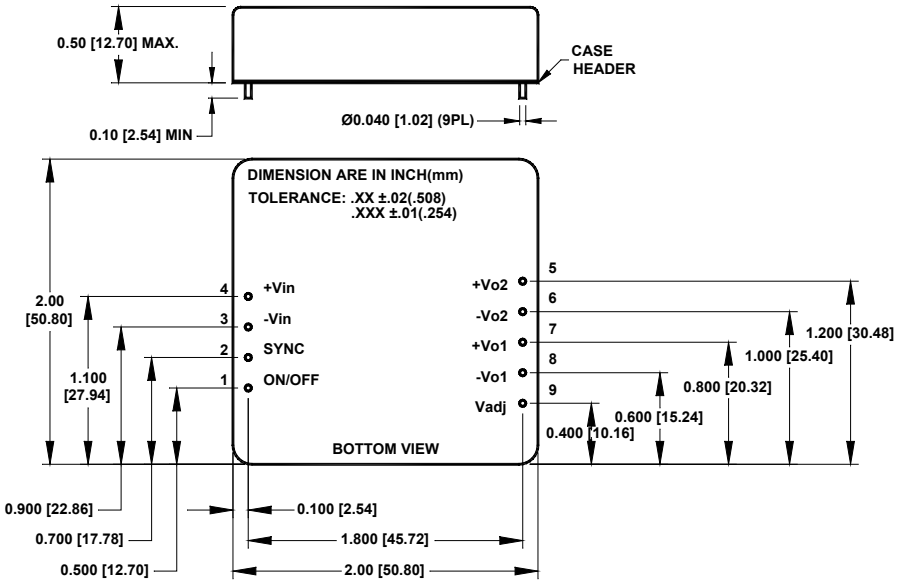
To trim the output voltage DOWN, connect a 5% ¼W 1.5MΩ resistor (DO NOT GO BELOW 1MΩ) between the $+V_{O1}$ (Pin 7) output and trim pin of the converter. To trim the output voltage UP, connect a 5% ¼W resistor between the $-V_{O1}$ (Pin 8) output and trim pins of the converter. For UP/DOWN trimming capability, connect a 2MΩ potentiometer between the + and - output pins, with the wiper arm connected to the trim pin.

The trim resistors/potentiometer can be connected at the converter output pins or the load. However, if connected at the load, the resistance of the runs becomes part of the feedback network which improves load regulation. If the load is some distance from the converter, the use of #20 gauge wire is recommended to avoid excessive voltage drop due to the resistance of the circuit paths. See our application notes:
DC-001: Testing Transient Response in DC/DC Converters
DC-004: Thermal Consideration for DC/DC Converters

EXTERNAL SYNCHRONIZATION

A TTL signal applied at the SYNC pin of the converter will synchronize the switching frequency of the converter to that of the TTL input signal. The external (TTL) frequency must be equal or higher than the converter's frequency. At the positive-going edge of the applied pulse, the internal power-switching transistor turns off and the PWM discharges its timing capacitor. At the negative-going edge, the PWM resumes normal operation. The minimum positive pulse width of the TTL signal must be 300nS minimum and its frequency between 150kHz and 180kHz. NOTE: Higher frequencies will reduce the efficiency of the converter and wide TTL pulses will force the PWM to follow the external TTL width modulation, which may effect regulation. A high TTL signal at the SYNC pin of the converter will turn the converter off. An internal pull-down resistor will keep this pin low when it is not used. A pulse differentiator (see Figure 4) can be used to shape a square wave sync signal as shown in Figure 3.

MECHANICAL SPECIFICATIONS



Pin	Function
	DUAL
1	ON/OFF
2	SYNC
3	$-V_{IN}$
4	$+V_{IN}$
5	$+V_O$
6	COMMON
7	COMMON
8	$-V_O$
9	V_{OUT} ADJ

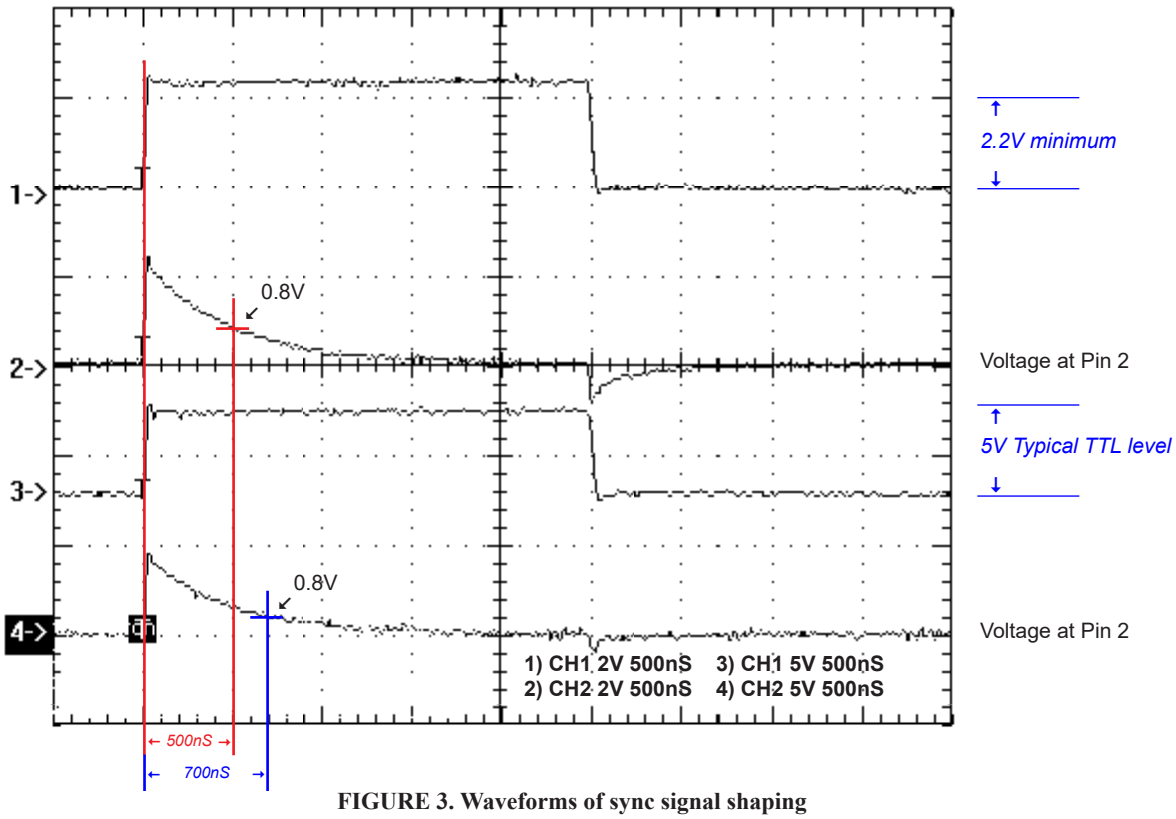


FIGURE 3. Waveforms of sync signal shaping

SYNC SIGNAL SHAPING

As described in External Synchronization, the PWM of the converter requires a TTL signal of 0.8Vdc to 2Vdc minimum amplitude and minimum duration of 300nS. When such a signal is not available (through one shot multivibrator or other pulse-shaping circuits) a C-R differentiator, such as the one in Figure 4, can be used to shape a square wave TTL signal. As is shown by the oscillogram in Figure 3, the positive edge of the sync pulse must be 2V minimum and the decaying exponential must reach the low 0.8Vdc in 300nS minimum from the positive edge. The parallel diode with the resistor is a small signal switching diode or a Schottky signal diode with 0.3 to 0.5V forward drop, it is used to clamp the voltage at pin 2@-0.5Vdc.

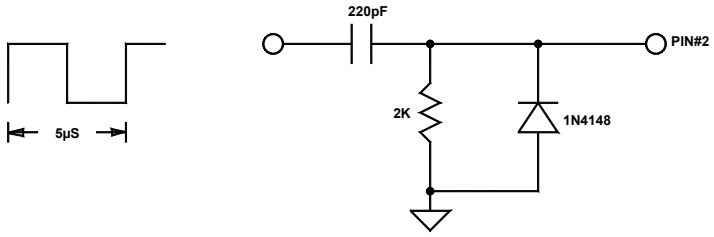


FIGURE 4. Suggested pulse-shaping circuit

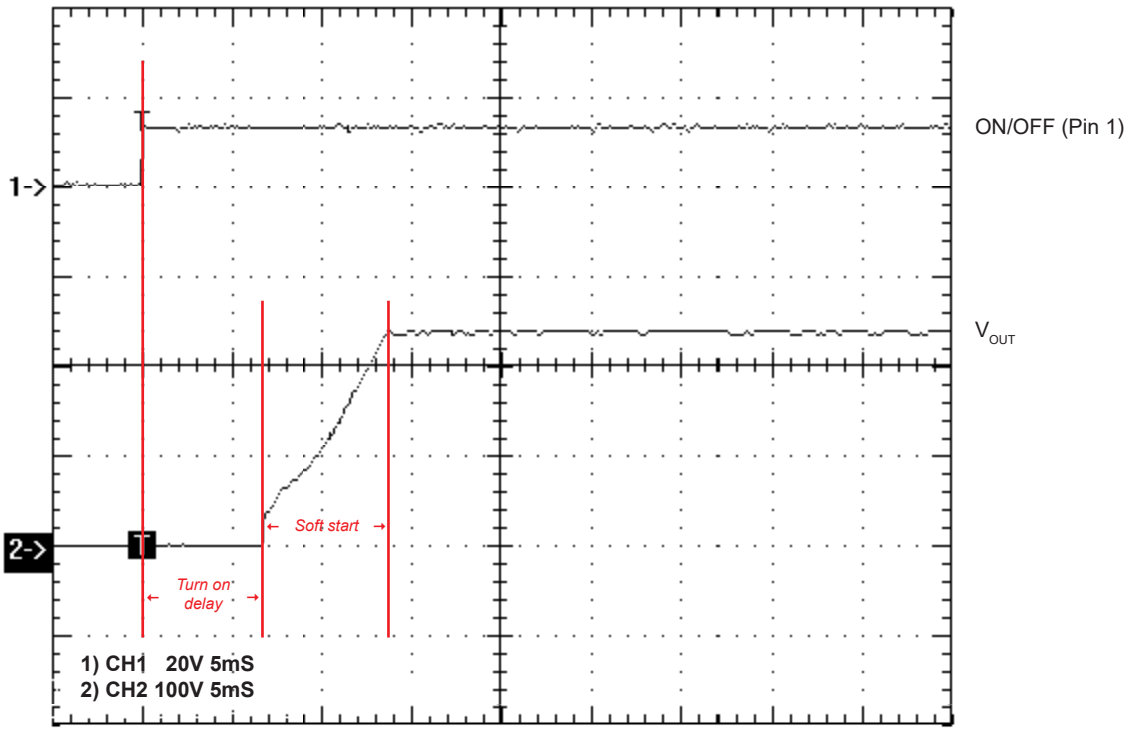


FIGURE 5. Turn on delay with soft start

SHORT CIRCUIT PROTECTION

The converter has a dual short circuit protection feature. At the input side of the converter, two short circuit current comparators are used to monitor the input current of the converter. They are biased at different voltage levels; the lower threshold (LTH) comparator provides the power limiting function of the converter. Under normal operating conditions, the LTH comparator limits the output power of the converter when the maximum output power is exceeded.

When a hard short is applied across the output of the converter and the input current exceeds the set threshold of the second comparator, the converter goes into shutdown mode, the overcurrent latch is set and the converter is turned off. The converter will turn on again when its input voltage is recycled (OFF-ON) or if the ON/OFF pin is used to turn the converter on and off. The time required for the ON/OFF pin to be held low is between 100mS and 800mS.